



## WSE-3



[click...]

**Cerebras Systems WSE-3 "wafer-scale" chip.**

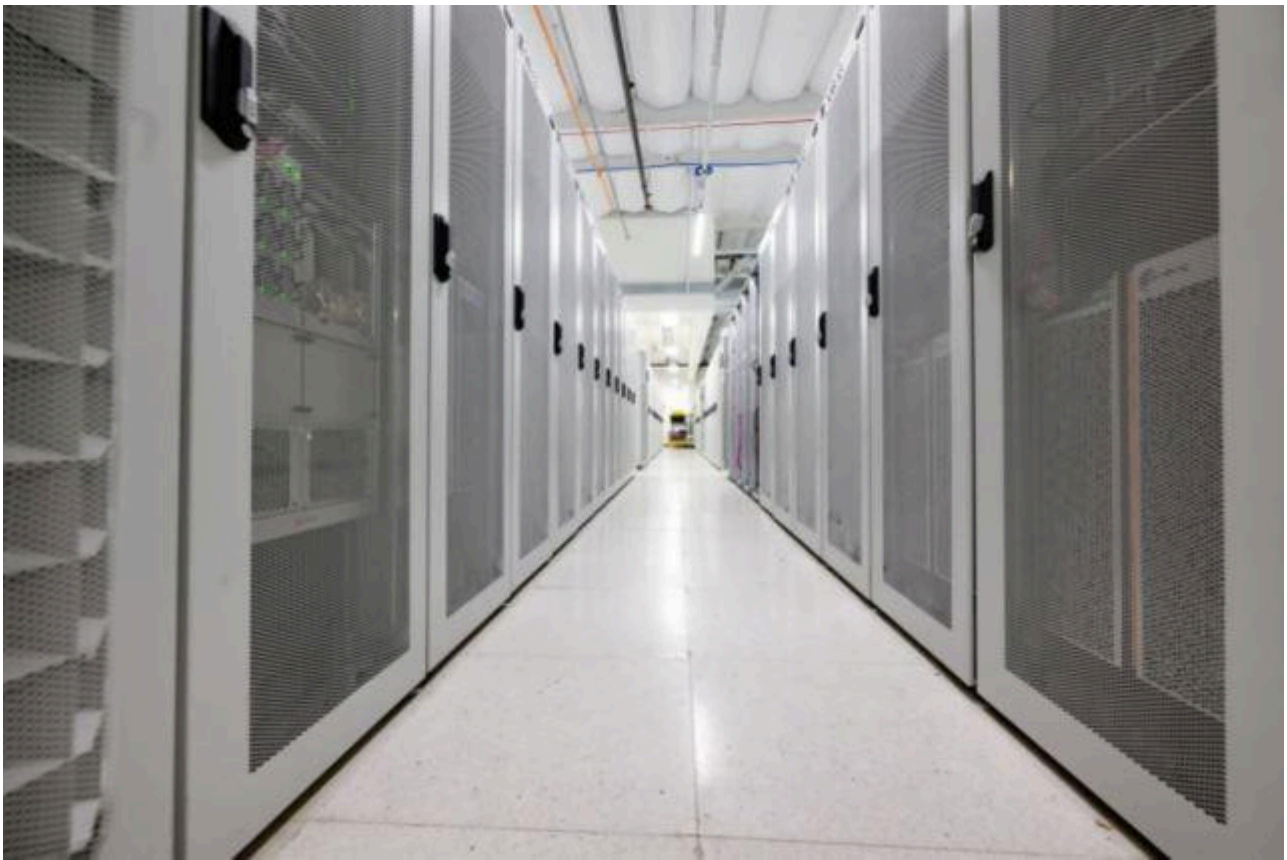
**5N TSMC process. 44GB on chip SRAM. 4 Billion Transistors...**

**125 petaflops 25 Trillion Parameters...**

**Can't wait for those 125 petaflop Smartphones...**

**What kind of Cosmic Ray Shielding does it have..?**

- [Cosmic Rays and Electronics](#)



- [EE Times](#)

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